

UNXVX 24W Series

AC Pin Interchangeable AC/DC Adaptor
AC Pin designed for one direction use.



Product Highlights

- Stability
- Energy and High Efficiency
- Small size
- Applicable to different countries
- Mobility

Protection

- Short Circuit Protection
- Over Voltage Protection
- Over Current Protection
- Over Temperature Protection (Optional)

Safety Standard

- 60950-1
- GB4943.1
- 62368-1
- PSE 別表第八 100V 基準に準拠

Efficiency

- Energy Efficiency Level VI (ErP / DoE)
- Meet COMMISSION REGULATION(EU) 2019/1782
- Meet DOE 10 CFR part 429 and 430

Emissions

- FCC
 - FCC Part15-B
- CE
 - EN(CISPR)55032-B
- VCCI-B

Immunity

- EN55035

The above specifications include the following test standards

 - ✓ EN61000-4-2
 - ✓ EN61000-4-3
 - ✓ EN61000-4-4
 - ✓ EN61000-4-5
 - ✓ EN61000-4-6
 - ✓ EN61000-4-8
 - ✓ EN61000-4-11

Electrical Spec

Input					
Description	Min.	Typ.	Max.	Units	Comment
Voltage	90	100~240	264	Vac	
Frequency	47	50/60	63	Hz	

Environmental					
Description	Min.	Typ.	Max.	Units	Comment
Operating Temperature	0	-	40	°C	Free Convection, Sea Level
Storage Temperature	-20	-	65	°C	Free Convection, Sea Level
Operating Humidity	5	-	95	%RH	No Condensing
Storage Humidity	5	-	95	%RH	No Condensing

Typical model list

Model Name	DC Output Voltage	DC Output Current	Output Voltage Precision	Ripple	Noise	Average Active Efficiency	No-Load Power Consumption
UNXVX3024-050030SA	5.0V	3.0A	±5%	150mV	150mV	81.39%	0.1W
UNXVX3024-120020SA	12.0V	2.0A	±5%	120mV	240mV	86.20%	0.1W
UNXVX3024-150016SA	15.0V	1.60A	±5%	240mV	240mV	86.20%	0.1W
UNXVX3024-240010SA	24.0V	1.0A	±5%	240mV	240mV	86.20%	0.1W

■ Measurement Condition

- Measurements shall be made with an oscilloscope with 20MHz bandwidth.
- Outputs shall be bypassed at the connector with a 0.1uF ceramic disk capacitor and a 10uF electrolytic capacitor to simulate system loading.

Mechanical Spec

UNXL Series	UNXZ Series

Mechanical Spec

UNXE Series	UNXR Series
UNXF Series	UNXK Series
UNXA Series	<p>the following blank</p>

■ Please contact our sales department for details of each model ■